1/25/05

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Term:

(deposition adj chamber) and (offset adj data adj structure) and (computer adj memor\$3) and (resolve adj differences adj between adj

Display:

50 Documents in Display Format: - Starting with Number 1

Generate: O Hit List O Hit Count O Side by Side O Image



Search History

DATE: Tuesday, January 25, 2005 Printable Copy Create Case

http://westbrs:9000/bin/gate.exe?state=ko2jc5.31.1&f=ffsearch

Set Name side by side	Query	Hit Count	Set Name result set
DB = B	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR		
<u>L19</u>	(deposition adj chamber) and (offset adj data adj structure) and (computer adj memor\$3) and (resolve adj differences adj between adj measurements)	2	<u>L19</u>
<u>L18</u>	(deposition adj chamber) and (offset adj data adj structure) and (computer adj memor\$3) and (first adj parameter) and (second adj parameter)	2	<u>L18</u>
<u>L17</u>	5871805.pn.	2	L17
<u>L16</u>	(deposit\$3 adj chamber) and (configur\$3) and (wafers) and (measur\$3 adj3 thickness adj3 wafer) and (pattern)	8	<u>L16</u>
<u>L15</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers)	29	<u>L15</u>
<u>L14</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (pattern adj5 thickness)	0	<u>L14</u>
<u>L13</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (wafer adj3 thickness)	0	<u>L13</u>
<u>L12</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (wafer adj3 thickness) and (pattern)	0	<u>L12</u>
<u>L11</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (wafer adj3 tickness) and (pattern)	0	<u>L11</u>
<u>L10</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (measur\$3 adj tickness) and (pattern)	0	<u>L10</u>
	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and		

Freeform Search			Page 2 of 2	
<u>L9</u>	(measur\$3 adj tickness) and (deposition adj pattern)	0	<u>L9</u>	
<u>L8</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (measur\$3 adj thickness) and (pattern)	28	<u>L8</u>	
<u>L7</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (thickness) and (pattern)	29	<u>L7</u>	
<u>L6</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (thickness) and (pattern)	29	<u>L6</u>	
<u>L5</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (thickness) and (pattern)	0	<u>L5</u>	
<u>L4</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj thickness) and (pattern)	0	<u>L4</u>	
<u>L3</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj thickness) and (deposition adj pattern)	0	<u>L3</u>	
<u>L2</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj tickness)	0	<u>L2</u>	
<u>L1</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj tickness) and (deposition adj pattern)	0	<u>L1</u>	

END OF SEARCH HISTORY